

FIG.3 51 speech recognizer speech decoder circular buffer 57 59 22 speech input/output part dialogue manager system controller clock generator list controller timer 61 67 69 63 memory contro-first memory second

35

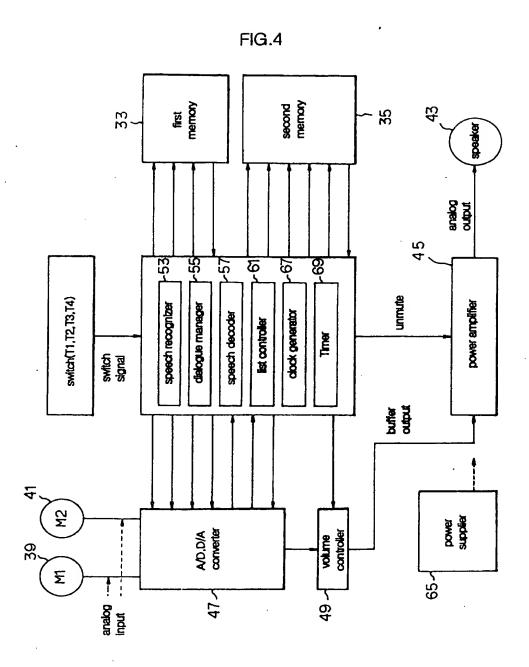


FIG.5

